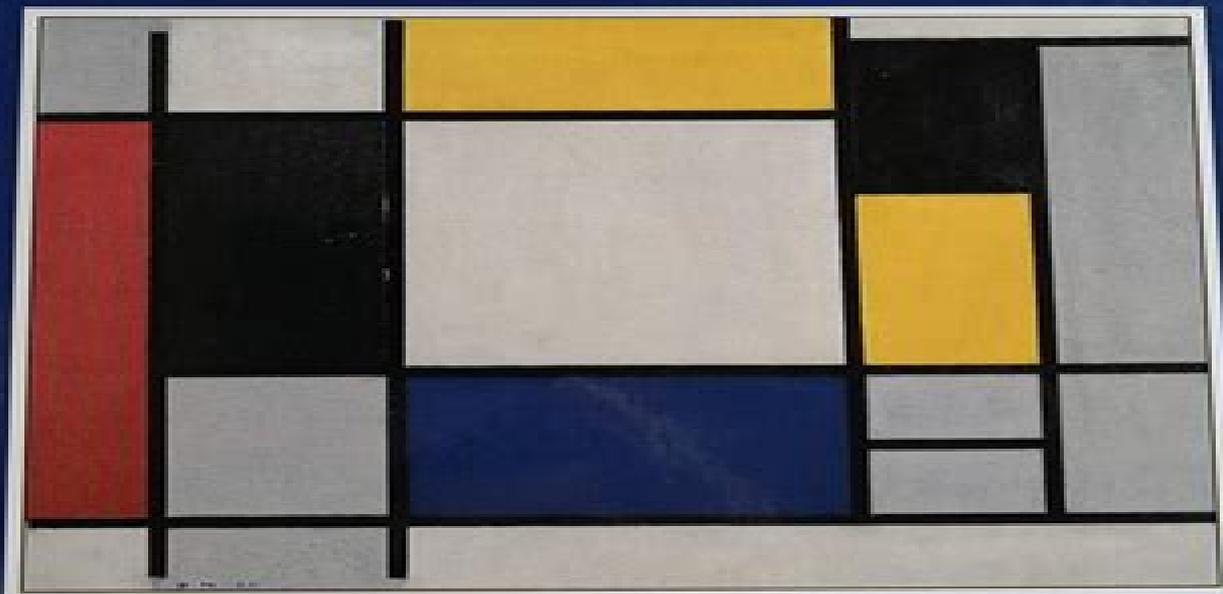


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expanding capabilities of underlying circuit technologies like CMOS but also by the dramatic increase in wireless communications products that depend on them Integrated Circuits for Wireless Communications includes seminal and classic papers in the field and is the first all in one resource to address this increasingly important topic Internationally known and highly regarded in the field editors Asad Abidi Paul Gray and Robert G Meyer have meticulously compiled more than 100 papers and articles covering the very latest high level integrated circuits techniques and solutions in use today Integrated Circuits for Wireless Communications is devised expressly to provide IC design engineers system architects and integrators with a practical understanding of subjects ranging from architecture choices for integrated transceivers to actual circuit designs in all viable IC technologies such as bipolar CMOS and GaAs The papers selected represent a breadth of coverage and level of expertise that is simply unmatched in the field Topics covered include Radio architectures Receivers Transmitters and transceivers Power amplifiers and RF switches Oscillators Passive components Systems applications

Digital Systems Jean-Pierre Deschamps,Elena Valderrama,Lluís Terés,2016-10-12 This textbook for a one semester course in Digital Systems Design describes the basic methods used to develop traditional Digital Systems based on the use of logic gates and flip flops as well as more advanced techniques that enable the design of very large circuits based on Hardware Description Languages and Synthesis tools It was originally designed to accompany a MOOC Massive Open Online Course created at the Autonomous University of Barcelona UAB currently available on the Coursera platform Readers will learn what a digital system is and how it can be developed preparing them for steps toward other technical disciplines such as Computer Architecture Robotics Bionics Avionics and others In particular students will learn to design digital systems of medium complexity describe digital systems using high level hardware description languages and understand the operation of computers at their most basic level All concepts introduced are reinforced by plentiful illustrations examples exercises and applications For example as an applied example of the design techniques presented the authors demonstrate the synthesis of a simple processor leaving the student in a position to enter the world of Computer Architecture and Embedded Systems

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