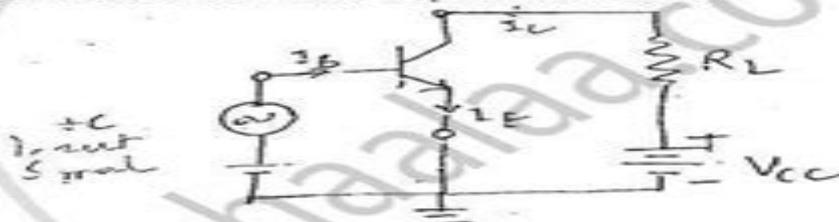


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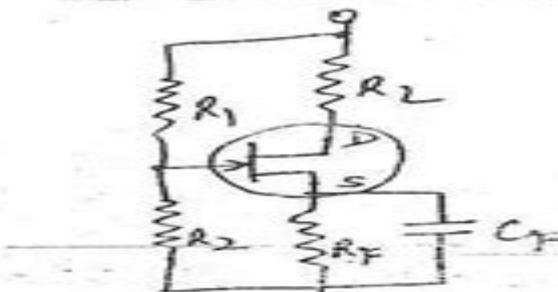
[ Total Marks : 100

- N.B. (1) Question No. 1 is compulsory.  
 (2) Attempt any four from Question No. 2 to Question No. 7.  
 (3) Figures to the right indicates full marks.

1. (a) Why Zener diode is used as regulator in Power supply? 20  
 (b) Differentiate between bridge rectifier and full wave center top rectifier.  
 (c) What are the advantages of negative feedback?  
 (d) Differentiate JFET and BJT.
2. (a) Explain the construction, equivalent circuit V-I characteristics and working of Schottky diode. 10  
 (b) A half wave rectifier uses a diode with a forward resistance of  $100\Omega$ . If the input ac voltage is 220 V rms and the load resistance is  $2\text{ k}\Omega$  determine— 10
  - (i)  $I_{max}$ ,  $I_{ac}$  and  $I_{rms}$ .
  - (ii)  $P/V$  when diode is ideal.
  - (iii) Load output voltage
  - (iv) dc output power.
3. (a) In CE configuration shown below, collector – supply voltage  $V_{CC} = 10V$ , load resistance  $R_L$  is  $8\text{ k}\Omega$ , draw the dc load line. Determine the operating point Q for zero signal if base current is  $15\mu\text{A}$  and  $\beta$  is 40. 10



- (b) What is meaning of biasing a transistor? Explain thermal runaway and hence importance of biasing with respect to a bijnunction transistor. 10
4. (a) What is transconductance? An N – channel JFET has a pinch off voltage of  $-4.5\text{ V}$  and  $I_{OSS} = 9\text{ mA}$ . At what value of  $V_{GS}$  will  $I_{OS}$  equal to  $3\text{mA}$ . What is its  $g_m$  at this  $I_{OS}$ . 10  
 (b) A JFET amplifier with stabilized biasing circuit as shown in Figure below has device parameters  $V_p = -2V$ ,  $I_{OSS} = 5\text{ mA}$ ,  $R_L = 910\Omega$ ,  $R_F = 2.20\text{ k}\Omega$ ,  $R_1 = 12\text{ m}\Omega$ ,  $R_2 = 8.57\text{ m}\Omega$  and  $V_{DD} = 24\text{ volt}$ . Find the value of drain current  $I_D$  at the operating point. Also verify that FET will operate in pinch off region. 10



# Basic Electronics Question Paper 2011

**Xiaolong Qi**



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## **Table of Contents Basic Electronics Question Paper 2011**

1. Understanding the eBook Basic Electronics Question Paper 2011
  - The Rise of Digital Reading Basic Electronics Question Paper 2011
  - Advantages of eBooks Over Traditional Books
2. Identifying Basic Electronics Question Paper 2011
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Basic Electronics Question Paper 2011
  - User-Friendly Interface
4. Exploring eBook Recommendations from Basic Electronics Question Paper 2011
  - Personalized Recommendations
  - Basic Electronics Question Paper 2011 User Reviews and Ratings

- Basic Electronics Question Paper 2011 and Bestseller Lists
- 5. Accessing Basic Electronics Question Paper 2011 Free and Paid eBooks
  - Basic Electronics Question Paper 2011 Public Domain eBooks
  - Basic Electronics Question Paper 2011 eBook Subscription Services
  - Basic Electronics Question Paper 2011 Budget-Friendly Options
- 6. Navigating Basic Electronics Question Paper 2011 eBook Formats
  - ePub, PDF, MOBI, and More
  - Basic Electronics Question Paper 2011 Compatibility with Devices
  - Basic Electronics Question Paper 2011 Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Basic Electronics Question Paper 2011
  - Highlighting and Note-Taking Basic Electronics Question Paper 2011
  - Interactive Elements Basic Electronics Question Paper 2011
- 8. Staying Engaged with Basic Electronics Question Paper 2011
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Basic Electronics Question Paper 2011
- 9. Balancing eBooks and Physical Books Basic Electronics Question Paper 2011
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Basic Electronics Question Paper 2011
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Basic Electronics Question Paper 2011
  - Setting Reading Goals Basic Electronics Question Paper 2011
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Basic Electronics Question Paper 2011
  - Fact-Checking eBook Content of Basic Electronics Question Paper 2011
  - Distinguishing Credible Sources

13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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